

data and common electrodes on the first substrate, the data and common electrodes being formed at an angle with respect to the longitudinal direction of the gate lines; and
a liquid crystal layer between the first and second substrates.

30. (NEW) The device of claim 29, further comprising a thin film transistor at a crossing portion of the gate and data lines.

31. (NEW) The device of claim 29, wherein the common line and the data electrode forms a storage capacitor.

32. (NEW) The device of claim 29, wherein the common electrode is slanted relative to the data line.

33. (NEW) The device of claim 29, wherein the data electrode is slanted relative to the data line.

34. (NEW) The device of claim 29, wherein the common electrode is slanted relative to the gate line.

35. (NEW) The device of claim 29, wherein the data electrode is slanted relative to the gate line.

36. (NEW) The device of claim 29, wherein the angle is greater than 0° and less than 90° .

37. (NEW) The device of claim 29, wherein the angle is about 85° .
38. (NEW) The device of claim 29, wherein the angle is greater than 90° and less than 180° .
39. (NEW) The device of claim 29, wherein the angle is about 95° .
40. (NEW) The device of claim 29, further comprising:
a light shielding layer on the second substrate; and
a color filter layer on the second substrate.
41. (NEW) The device of claim 40, wherein the light shielding layer includes Cr.
42. (NEW) The device of claim 40, wherein the light shielding layer includes Cr/CrOx.
43. (NEW) The device of claim 29, further comprising a retardation film on at least one of the first and second substrates.
44. (NEW) The device of claim 29, wherein the common electrode extends from the common line.

45. (NEW) A liquid crystal display device having a plurality of pixels each including at least one first and second regions, comprising:

first and second substrates;

data and common electrodes on the first substrate in each first and second region, with an elongation direction of the data and common electrodes being formed with respect to an

longitudinal direction of an electric field direction of the data and common electrode; and

a liquid crystal layer between the first and second substrates.

46. (NEW) The device of claim 45, further comprising gate and data lines on the first substrate.